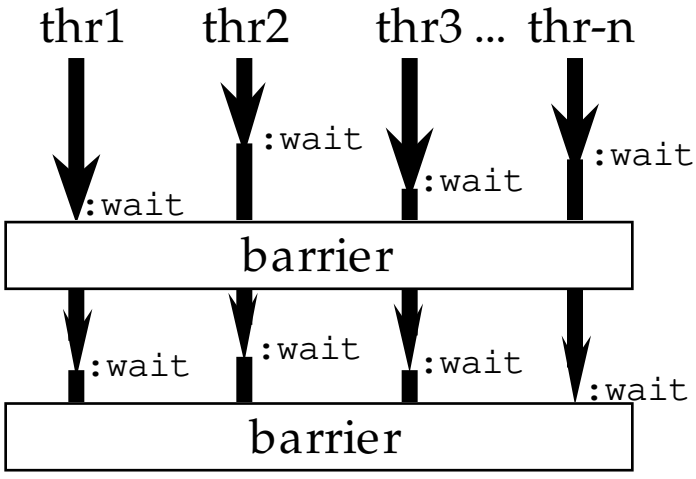
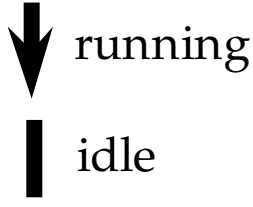
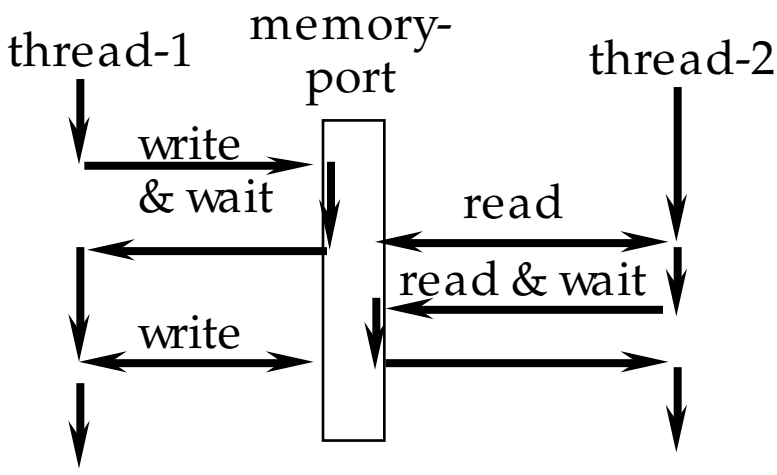



(a) Barrier-synch




(b) Synchronized-
memory port